

Series G BiPotentiostat Configuration

Introduction

The **Series G** BiPotentiostat requires the installation of a single jumper and an internal timing cable between the two control boards. The **Reference 600** and **PCI/FAS2** do not require these components. This *instrument specific* note will detail the process of installing these components on a pair of Series G potentiostats.

Installation

The timing cable is part number 986-00057. It is shown in Figure 1. One side is designed to connect to the Master potentiostat and the other is designed to connect to the Serf potentiostat. Please use the following steps:

- 1) Locate the two potentiostat control boards. You will need to designate one as the Master and one as the Serf (make note of the serial numbers for reference). Figure 2 shows the upper left corner of the control board and the location of the relevant connections.
- 2) Connect the Master end of the timing cable to the Master potentiostat and the Serf end of the timing cable to the Serf potentiostat on connector J400. If the cable is not labeled consult Figure 3 to determine which end is the Master.
- 3) On the Serf potentiostat, locate the pin labeled J402, shown highlighted in Figure 1. Install a jumper at J402. *Do not install a jumper on the Master potentiostat.*
- 4) Refer to "BiPotentiostat Installation.pdf" for instructions on installation of the BiPotentiostat experiment scripts.

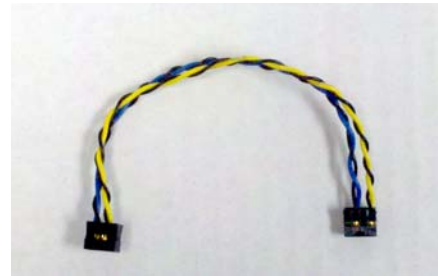


Figure 1. Internal Timing Cable for Series G BiPotentiostat (986-00057).

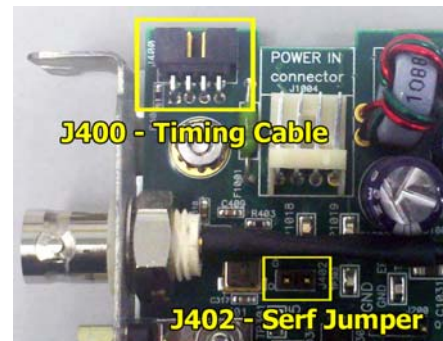


Figure 2. Series G Control Board, J400 and J402 Highlighted.

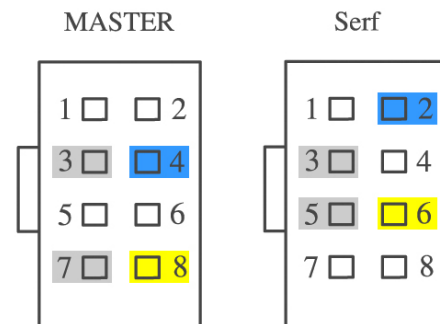


Figure 3. Schematic of Internal Timing Cable.

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